

## AMENDMENTS TO THE SPECIFICATION

please replace the paragraph beginning on page 8, line 3 with the following paragraph:

While in the state 58, the process 50 may mark pins associated with each member after the pinout is completed. The process 50 may generate a pinout specific to a particular device member by marking (or customizing) ~~the~~ a superset grid (illustrated in FIGS. 2 and 3) for each member using information generated at the state 54 (e.g., information regarding which pin is applicable to which device). For example, a single A may be applicable to both members MEMBERA and MEMBERB, while C may be applicable to member MEMBERA. Pins not applicable to a particular member device may be marked "no-connect" (or NC) for the device. Users may then optionally ignore the no-connect pins if using one specific member of the family. Therefore, the process 50 may provide a footprint that may be common to all members in the family and a superset pinout that accommodates the needs of all members.